

Product Specification

Document No.: NMD-SPC- SK6812RARE

Product No.: SK6812RARE

Description: 4.2x4.6x1.3 mm 0.2W Intelligent external control surface mount SMD LED (MSL:5a)

Rev. No.: A/0

Date: 2022-02-25



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Product Overview :

SK6812RARE is a smart LED control circuit and light emitting circuit in one controlled LED source, which has the shape of a4246 LED chip. Each lighting element is a pixel, and the intensities of the pixels are contained within the intelligent digital interface input. The output is driven by patented PWM technology, which effectively guarantees high consistency of the color of the pixels. The control circuit consists of a signal shaping amplification circuit, a built-in constant current circuit, and a high precision RC oscillator.

The data protocol being used is unipolar RZ communication mode. The 24-bit data is transmitted from the controller to DIN of the first element, and if it is accepted it is extracted pixel to pixel. After an internal data latch, the remaining data is passed through the internal amplification circuit and sent out on the DO port to the remaining pixels. The pixel is reset after the end of DIN. Using automatic shaping forwarding technology makes the number of cascaded pixels without signal transmission only limited by signal transmission speed.

The LED has a low driving voltage (which allows for environmental protection and energy saving), high brightness, scattering angle, good consistency, low power, and long life. The control circuit is integrated in the LED above.

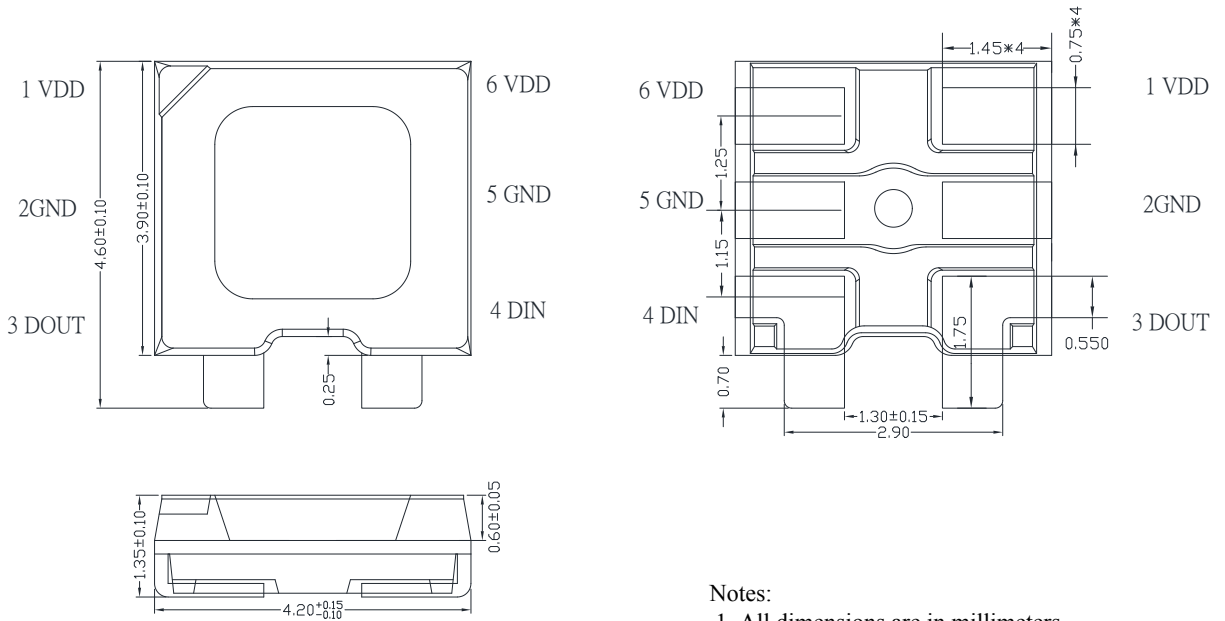
2. Main Application Field:

- Full color LED string light, LED full color module, LED super hard and soft lights, LED guardrail tube, LED appearance / scene lighting
- LED point light, LED pixel screen, LED shaped screen, a variety of electronic products, electrical equipment etc.

3. Description:

- Top SMD internal integrated high quality external control line serial cascade constant current IC;
- control circuit and the RGB chip in SMD 4246 components, to form a complete control of pixel, color mixing uniformity and consistency;
- built-in data shaping circuit, a pixel signal is received after wave shaping
- The built-in power on reset and reset circuit, the power does not work;
- gray level adjusting circuit (256 level gray scale adjustable);
- red drive special treatment, color balance;
- line data transmission;
- plastic forward strengthening technology, the transmission distance between two points over 10M;
- Using a typical data transmission frequency of 800 Kbps, when the refresh rate of 30 frames per sec.

4. Mechanical Dimensions:



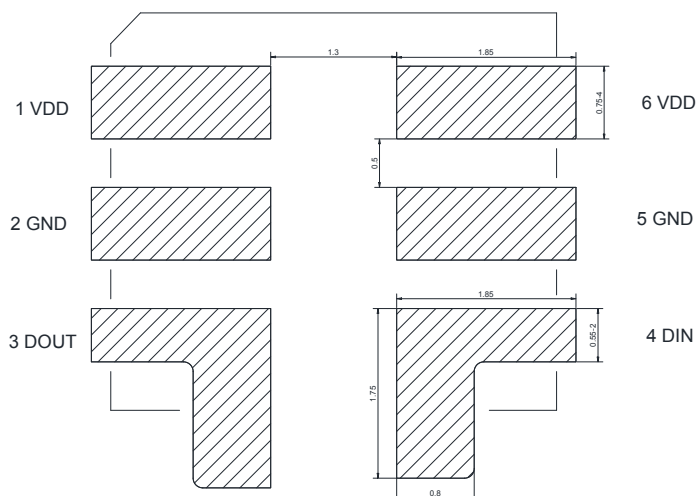
Notes:

1. All dimensions are in millimeters.
2. Tolerance is ± 0.1 mm unless otherwise noted

5. PIN configuration

NO.	Symbol	Function description
1	VDD	Power supply LED
2	GND	Control data signal output
3	DOUT	Ground
4	DIN	Control data signal input
5	GND	Control data signal output
6	VDD	Power supply LED

6. Recommended dimensions for PCB



7. General description of product naming.

SK 6812-RARE

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①	②	③
Series	IC series and current code	Internal coding
The default is to integrate the RGB chip with the IC in the package outline	Refers to the 68 series IC 12MA current version	4.2x4. 6x1. 35mm outline package

8. Electrical parameters (Ta=25°C,VSS=0V) :

Parameter	Symbol	Range	Unit
Power supply voltage	VDD	+3.7~+5.5	V
Logic input voltage	VIN	-0.5~VDD+0.5	V
Working temperature	Topt	-40~+85	°C
Storage temperature	Tstg	-40~+85	°C
ESD pressure(DM)	VESD	200	V
ESD pressure(HBM)	VESD	2K	V

9. Electrical/Optical Characteristics:

Color	SK6812-RARE 12mA	
	Dominate Wavelength(nm)	Luminance(mcd)
Red	620-630	/
Green	520-535	/
Blue	460-475	/

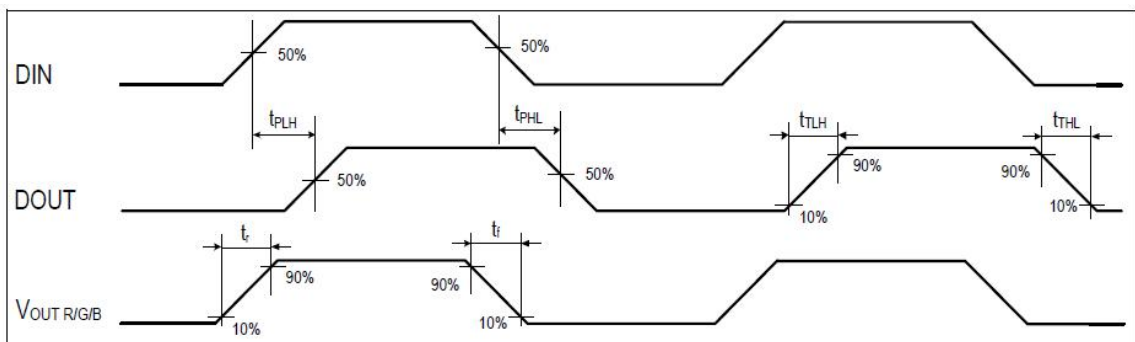
10. The IC electrical parameters (unless otherwise specified, TA=-20 ~ +70 °C, VDD=4.5 ~ 5.5V, VSS=0V):

Parameter	Symbol	Min	Typical	Max	Unit	Test conditions
The chip supply voltage	VDD	---	5.2	---	V	---
The signal input flip threshold	VIH	2.8	---	---	V	VDD=5V, Adjust DIN input level
	VIL	---	---	1.6	V	
The frequency of PWM	FPWM	---	1	---	KHZ	---
Static power consumption	IDD	---	0.35	---	mA	VDD = 4.5V, IOU "OFF"---
OUT R/G/B Constant current inflection point voltage	VVDS_S	---	0.6	---	V	IOU = 12mA
OUT R/G/B Output current variation	%VS.VDS	---	0.5	---	%	IOU = 12mA, VDS =1.0~3.0V
	%VS.VDD	---	0.5	---	%	IOU =12mA, VDD= 4.5~5.5V
	%VS.T	---	5	---	%	IOU = 12mA, TA=-40~+85°C
OUT R/G/B Port leakage current	Ileak	---	---	1	uA	VDS =15V, IOU "OFF"

11. Switching characteristics(VCC=5V Ta=25 °C):

Parameter	Symbol	Min	Typical	Max	Unit	Test conditions
The speed of data transmission	fDIN	---	800	---	KHZ	The duty ratio of 67% (data 1)
Signal transmission delay (Note 4)	TPLH	---	67	---	ns	DOUT Port to ground load capacitance 30pF, Signal transmission delay from DIN to DOUT
	TPHL	---	93	---	ns	
Dout conversion time (Note 5)	TTLH	---	15	---	ns	DOUT Port to ground load capacitance 30pF
	TTHL	---	23	---	ns	
OUT R/G/B Conversion time (Note 6)	Tr	---	104	---	ns	IOU R/B =5mA, OUT R/G/B Serial connection of ports 200 Ω resistance to VDD, load capacitance to ground 30pF
	Tf	---	298	---	ns	

Note 4, note 5 and note 6: as shown in the figure below



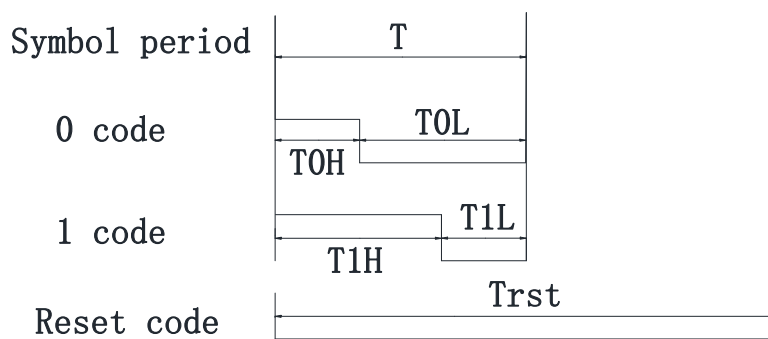
12. The data transmission time (TH+TL=1.25μs±600ns):

Name		Min.	Standard value	Max.	Unit
T	Code period	1.20	--	--	μs
T0H	0 code, high level time	0.2	0.30	0.4	μs
T0L	0 code, low level time	0.8	--	--	μs
T1H	1 code, high level time	0.65	0.75	1.0	μs
T1L	1 code, low level time	0.2	--	--	μs
Trst	Reset code, low level time	>200	--	--	μs

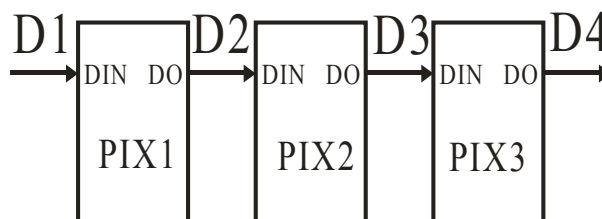
1. The protocol uses a unipolar zeroing code. Each symbol must have a low level. Each symbol in this protocol starts with a high level. The high time width determines the "0" or "1" code. .
2. When writing programs, the minimum symbol period is 1.2μs.
3. The high time of "0" code and "1" code should be in accordance with the stipulated range in the above table. The low time requirement of "0" code and "1" code is less than 20μs.

13. Timing waveform:

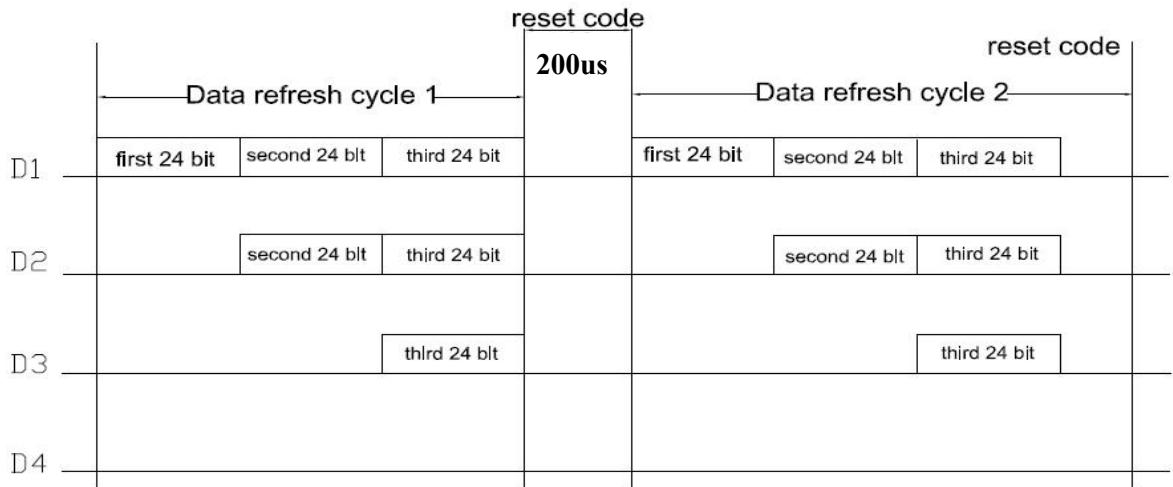
Input code:



Connection mode:



14. The method of data transmission:



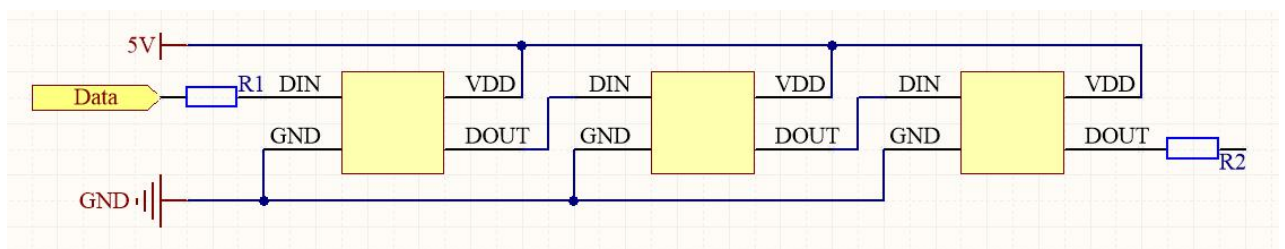
Note: D1 is the data sent by MCU, and D2, D3 and D4 are the data automatically shaped and forwarded by cascade circuit

15. The data structure of 24bit:



Note: high starting, in order to send data (G7 - G6 - B0)

16. The typical application circuit:



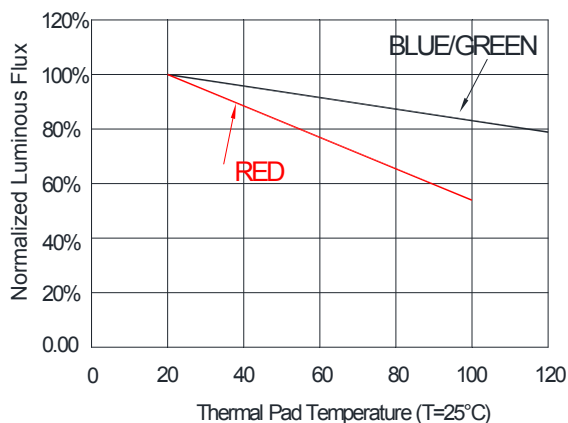
In the practical application circuit, the signal input and output pins of the IC signal input and output pins should be connected to the signal input and output terminals. In addition, in order to make the IC chip is more stable, even the capacitance between beads is essential back;

Application: used for soft lamp strip or hard light, lamp beads transmission distance is short, suggested in signal in time the clock line input and output end of each connected in series protection resistors, R1=R2 of about 500 ohms.

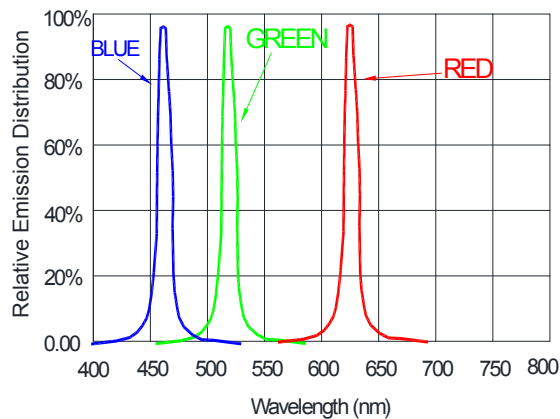
Application: for module or general special-shaped products, lamp beads transmission distance is long, because of different wire and transmission distance, in the signal in time clock at both ends of the line on grounding protection resistance will be slightly different; to the actual use of fixed;

17. Standard LED Performance Graph:

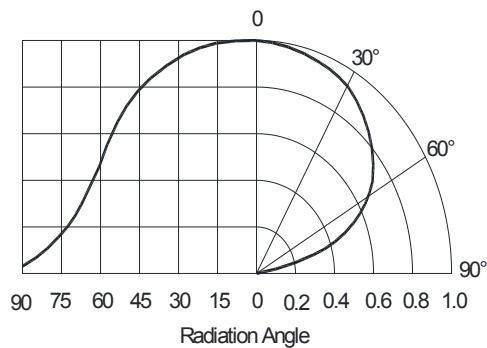
Thermal Pad Temperature vs. Relative Light Output



Wavelength Characteristics

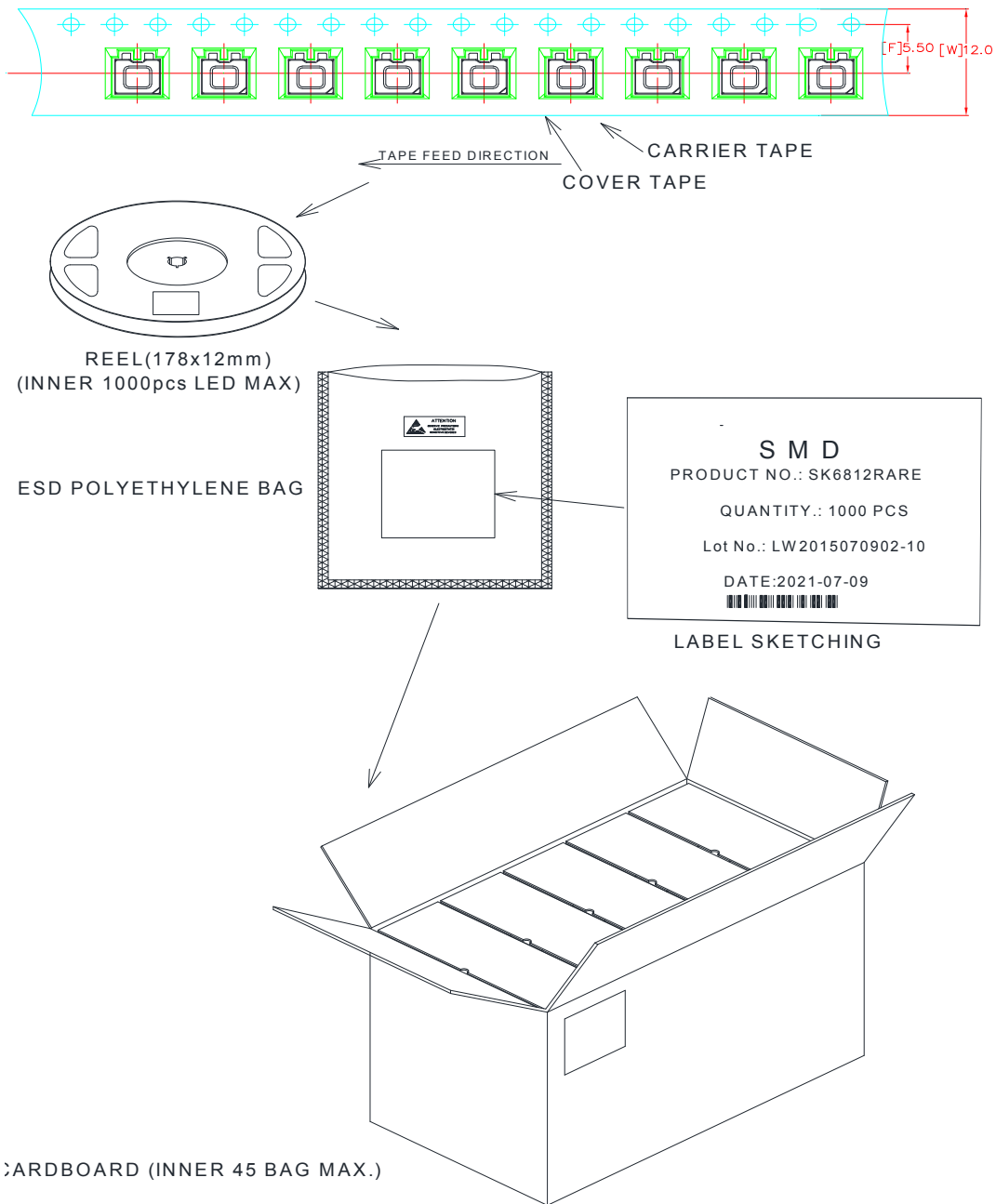


Typical Radiation Pattern 120°



18. Packaging Standard:

SK6812RARE-000



The reel pack is applied in SMD LED. The LEDs are packed in cardboard boxes after packaging in normal or anti-electrostatic bags. cardboard boxes will be used to protect the LEDs from mechanical shocks during transportation. The boxes are not water resistant and therefore must be kept away from water and moisture.

19. Reliability Test :

NO.	Test item	Test Conditions	Reference	Criterion
1	Thermal Shock	100 ± 5° C ~ -40° C ± 5° C 30min~30min 100 cycles	MIL-STD-202G	0/22
2	High Temperature Storage	Ta= +100°C 1000hrs	JEITA ED-4701 200 201	0/22
3	Low Temperature Storage	Ta= -40°C 1000hrs	JEITA ED-4701 200 202	0/22
4	High Temperature High Humidity Storage	Ta=60°C RH=90% 1000hrs	JEITA ED-4701 100 103	0/22
5	Temperature Cycle	-40°C~25°C~100°C~25°C 30min~5min~30min~5min 100 cycles	JEITA ED-4701 100 105	0/22
6	Resistance to Soldering Heat	Tsld = 260° C, 10sec. 2 times	JEITA ED-4701 300 301	0/22
7	Room temp Life Test	25° C, IF: Typical current , 1000hrs	JESD22-A 108D	0/22

Criteria for Judging the Damage:

Item	Symbol	Test Condition	Limit	
			Min	Max
Luminous Intensity	IV	DC=5V, Typical current	Init. Value*0.7	---
Resistance to Soldering Heat	---	DC=5V, Typical current	No dead lights or obvious damage	