Product Specification

Document number: NMD-SPC-SK6805-EC3210F

Product model: SK6805-EC3210F

Product description: 3.2x1.0x1.08mm Type 0.1Watt Power Embedded

（MSL： 4）

Version number: 01

Time: 2021-03-29

ELECTROSTATIC
SENSITIVE DEVICES

RoHS

Caution
Moisture-Sensitive

Shenzhen Normand Electronic Co.,Ltd

CONTENTS

Shenzhen Normand Electronic Co.,Ltd

1. Product Overview :

SK6805-EC3210F is an embedded control LED light source, which includes intelligent digital, digital interface, data latching, signal shaping and amplifying driver, power supply voltage stabilizing circuit, built-in constant current circuit, high precision RC oscillator, the output drive uses the patent PWM technology, effectively guarantees the pixel point inside the light color high consistency. The data protocol adopts the communication method of Unipolar Return Code. After the Pixel is reset on the power, the DIN terminal receives the data transmitted from the controller. The first 24 bit data sent is extracted by the first pixel and sent to the Pixel, the rest of the data is amplified by the internal shaping, then the data is rotated through the DO port, and the output is sent to the next cascaded pixels, and the signal is reduced by 24bit for each pixel. Pixel, the use of automatic shaping forwarding technology, so that the number of the Pixel cascade is limited by the signal transmission, only limited by the signal, the transmission speed requirements.

2.Main features
● **Easy To Design**
● **Easy To Build**
● **Easy To Program**

3. Description:

● **Chip SMD internal integrated high quality external control line serial cascade constant current IC;**

● **control circuit and the RGB chip in EC3210 components, to form a complete control of pixel, color mixing uniformity and consistency;**

●**built-in data shaping circuit, a pixel signal is received after wave shaping and output waveform distortion will not guarantee a line;**

●**The built-in power on reset and reset circuit, the power does not work;**

●**gray level adjusting circuit (256 level gray scale adjustable);**

● **red drive special treatment, color balance;**

● **line data transmission;**

● **plastic forward strengthening technology, the transmission distance between two points over 10M;**

●**Using a typical data transmission frequency of 800 Kbps, when the refresh rate of 30 frames per sec**
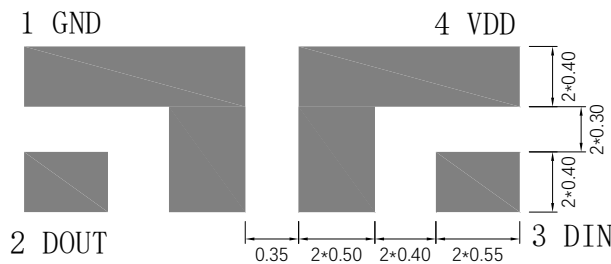
4. Mechanical Dimensions:

BOTTOM VIEW
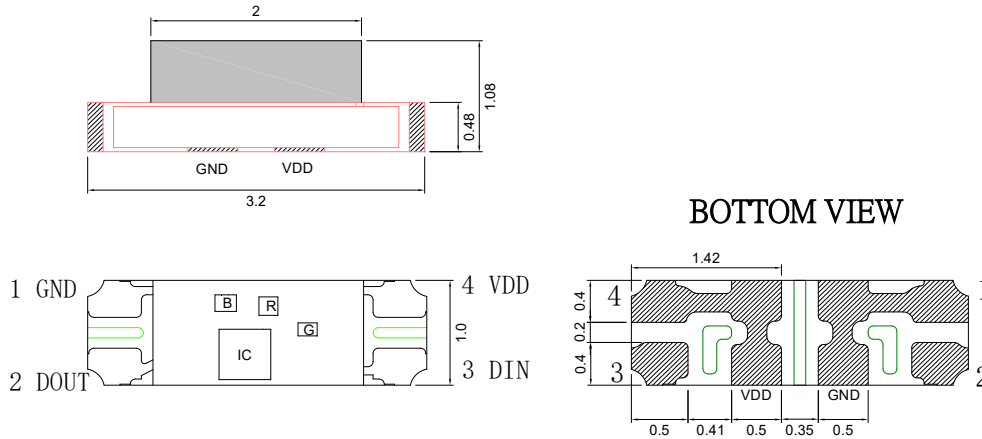
Notes:
1. All dimensions are in millimeters.
2. Tolerance is ±0.05mm unless otherwise noted

5. PIN configuration：
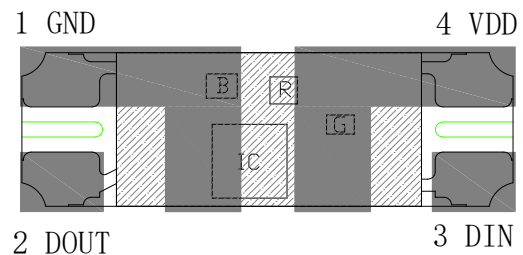
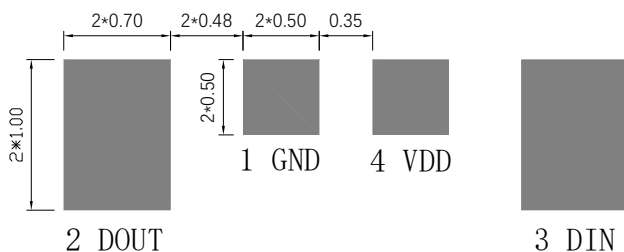| NO.	Symbol	Function description
1	GND	Ground
2	DOUT	Control data signal output
3	DIN	Control data signal input
4	VDD	Power supply LED

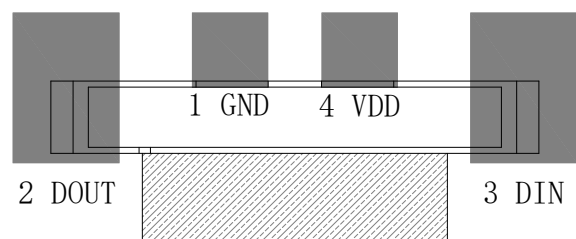## 6. Recommended dimensions for PCB：

Top patch suggested pad

Top mounting diagran

Side patch suggested pad

Sketch map of side mounting

7. General description of product naming

$$\underset{①}{\underline{\text{SK}}} \quad \underset{②}{\underline{\text{6805}}} \quad \text{-} \underset{③}{\underline{\text{EC3210}}} \quad \underset{④}{\underline{\text{F}}}$$

①	②	③	④
series	IC series and current code	Package shape	Lens shape
By default, RGB chip is integrated with IC	Refers to 68 series IC 6805: refers to 5mA current version	3.2x1.0x1.48mm PCB bracket packaging	F:means square Lens

8. Electrical parameters （Ta=25℃,VSS=0V）:

Parameter	Symbol	Range	Unit
Power supply voltage	VDD	+3.7～+5.5	V
Logic input voltage	V_{IN}	-0.5～VDD+0.5	V
Working temperature	Topt	-40~+80	℃
Storage temperature	Tstg	-40~+80	℃
ESD pressure(HBM)	V_{ESD}	2K	V
ESD pressure(DM)	V_{ESD}	200	V

9. Electrical/Optical Characteristics:

| Color	SK6805-EC3210R 5mA	
	Dominate Wavelength(nm)	Luminance(mcd)
Red	620-630	80-160
Green	520-535	240-450
Blue	460-475	60-120

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10. IC The electrical parameters (unless otherwise specified, TA=-20 ~ +70 ℃, VDD=4.5 ~ 5.5V,　VSS=0V):

Parmeter	Symbol	Min	Typical	Max	Unit	Test conditions
The chip supply voltage	VDD	---	5.2	---	V	---
The signal input flip threshold	VIH	0.7*VDD	---	---	V	VDD=5.0V
	VIL	---	---	0.3*VDD	V	
The frequency of PWM	FPWM	---	4.0	---	KHZ	---
Static power consumption	IDD	---	0.25	---	mA	---

11. Switching characteristics (VCC=5V Ta=25 ℃):

Parameter	Symbol	Min	Typical	Max	Unit	Test conditions
The speed of data transmission	fDIN	---	800	---	KHZ	The duty ratio of 67% (data 1)
DOUT transmission delay（Note 4）	TPLH	---	100	---	ns	The load capacitance of dout port to ground is 30pf,Signal transmission delay from DIN to dout
	TPHL	---	100	---	ns	
DOUT Conversion time（Note 5）	TTLH	---	15	---	ns	The load capacitance of dout port to ground is 30pf
	TTHL	---	24	---	ns	
IOUT Rise/Drop Tim（Note 6）	Tr	---	200	---	ns	IOUT = 5mA, out R / g / B port series connection 200 Ω electricResistance to VDD, load capacitance to ground 30pF
	Tf	---	280	---	ns	

Note 4、 Note 5、 Note 6： As shown in the figure below

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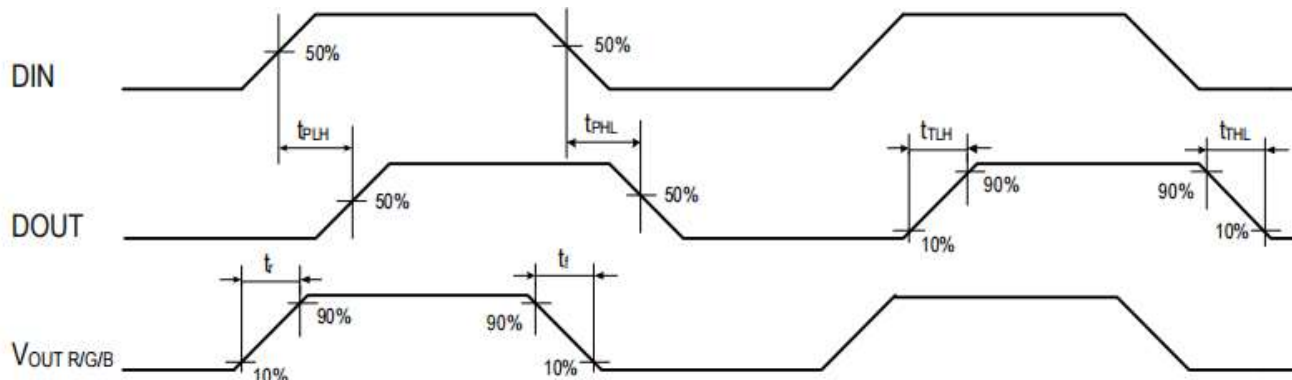
12. The data transmission time :

Name		Typical value	Tolerance range	Unit
T	Code period	1.2	--	μs
T0H	0 code, high level time	0.3	±0.05	μs
T0L	0 code, low level time	0.9	±0.05	μs
T1H	1 code, high level time	0.9	±0.05	μs
T1L	1 code, low level time	0.3	±0.05	μs
Trst	Reset code，low level time	＞200	--	μs

1. The protocol uses a unipolar zeroing code. Each symbol must have a low level. Each symbol in this protocol starts with a high level. The high time width determines the "0" or "1" code. .
2. When writing programs, the minimum symbol period is 1.2μs.
3. The high time of "0" code and "1" code should be in accordance with the stipulated range in the above table. The low time requirement of "0" code and "1" code is less than 20μs.

13. Timing waveform:

Input code:
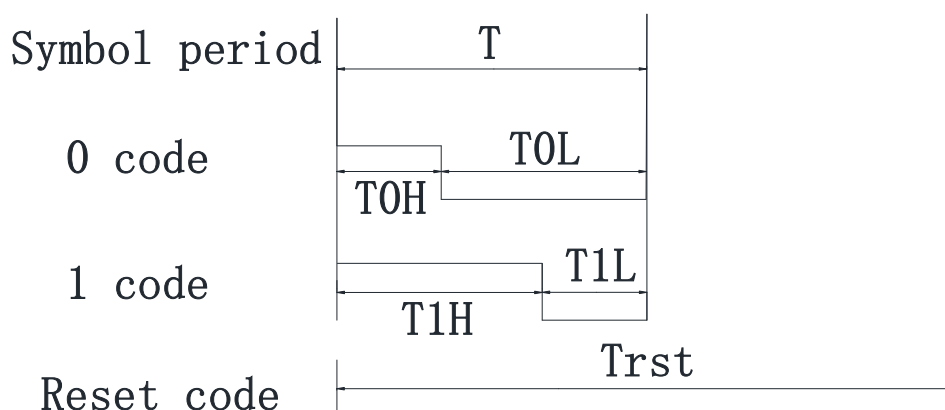
Connection mode:
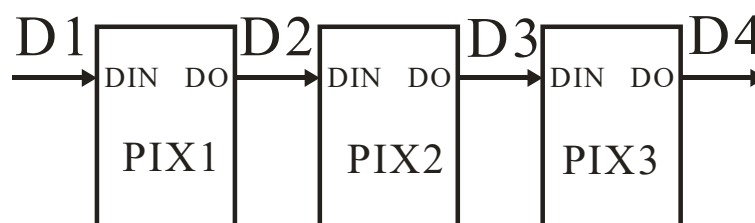
14. The method of data transmission:

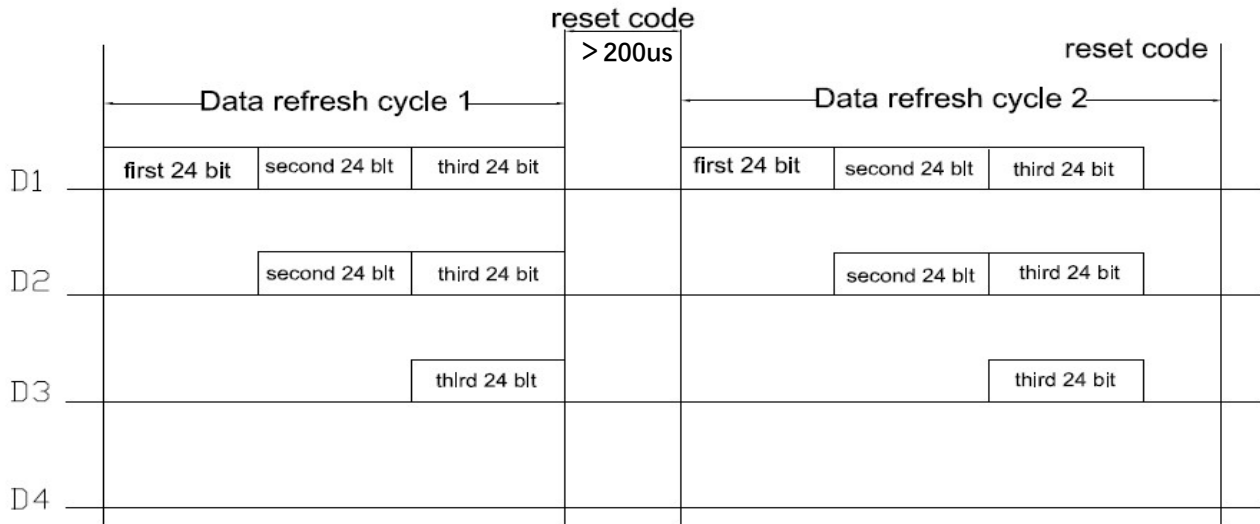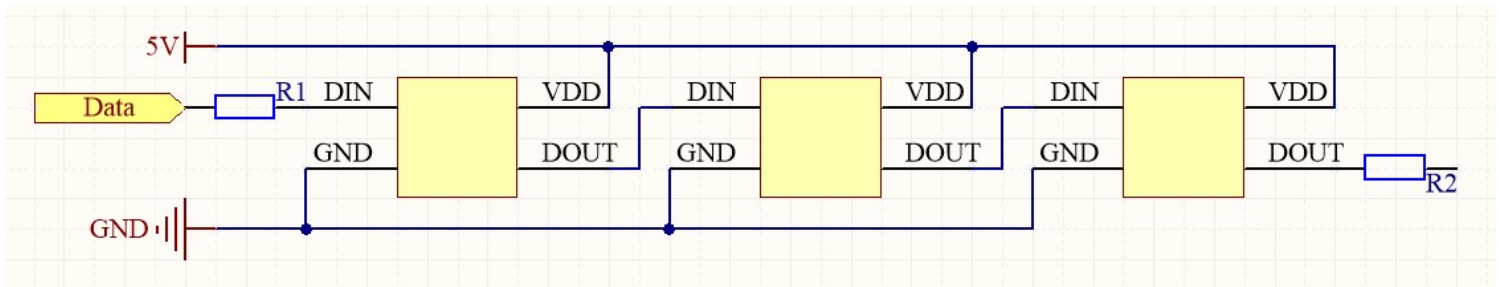
Note: the D1 sends data for MCU, D2, D3, D4 for data forwarding automatic shaping cascade circuit.

15. The data structure of 24bit:

G7	G6	G5	G4	G3	G2	G1	G0	R7	R6	R5	R4
R3	R2	R1	R0	B7	B6	B5	B4	B3	B2	B1	B0

Note: high starting, in order to send data (G7 - G6 - ....B0)

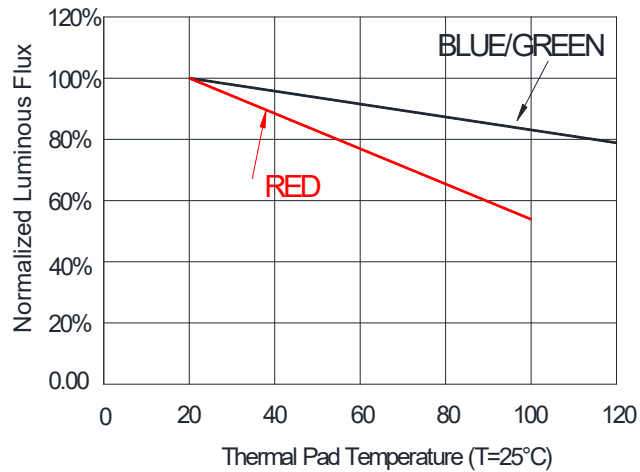## 16. The typical application circuit:

   In the practical application circuit, the signal input and output pins of the IC signal input and output pins should be connected to the signal input and output terminals. In addition, in order to make the IC chip is more stable, even the capacitance between beads is essential back;
Application: used for soft lamp strip or hard light, lamp beads transmission distance is short, suggested in signal in time the clock line input and output end of each connected in series protection resistors, R1 of about 500 ohms.
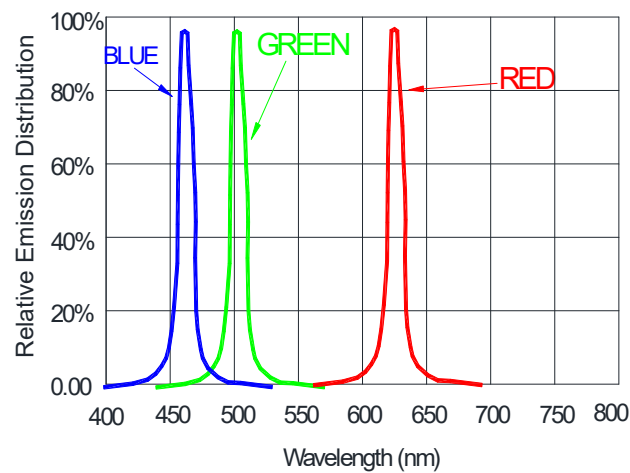Application: for module or general special-shaped products, lamp beads transmission distance is long, because of different wire and transmission distance, in the signal in time clock at both ends of the line on grounding protection resistance will be slightly different; to the actual use of fixed;

17. Standard LED Performance Graph:

Thermal Pad Temperature vs. Relative Light Output

Graph: Y-axis "Normalized Luminous Flux" (0.00 to 120%), X-axis "Thermal Pad Temperature (T=25°C)" (0 to 120). Shows BLUE/GREEN curve and RED curve.

Wavelength Characteristics

Graph: Y-axis "Relative Emission Distribution" (0.00 to 100%), X-axis "Wavelength (nm)" (400 to 800). Shows BLUE, GREEN, and RED emission peaks.

Typical Radiation Pattern   160°

Polar plot: Radiation angles 0°, 30°, 60°, 90°. X-axis "Radiation Angle" (90 75 60 45 30 15 0 0.2 0.4 0.6 0.8 1.0).

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18. Packaging Standard:

SK6805-EC3210F

CATHODE IDENTIFICATION

1.75

2

1.5

4

(B0)3.4

12

5.5

(A0)1.6

4

TAPE FEED DIRECTION

CARRIER TAPE

COVER TAPE

3 — 3 DIN
4 — 4 VDD
1 — 1 GND
2 — 2 DOUT

REEL(178x12mm)
(INNER 3000PCS LED MAX)

ESD POLYETHYLENE BAG

CHIP LED

PRODUCT NO: SK6805-EC3210F
QUANTITY: 3000 PCS
Lot No: LW20180713002-1
DATE: 2021-01-13

LABEL SKETCHING

CARDBOARD (INNER 40 BAG MAX.)

The reel pack is applied in SMD LED. The LEDs are packed in cardboard boxes after packaging in normal or anti-electrostatic bags. cardboard boxes will be used to protect the LEDs from mechanical shocks during transportation. The boxes are not water resistant and therefore must be kept away from water and moisture.

19. Reliability Test :

NO.	Test item	Test Conditions	Reference	Criterion
1	Thermal Shock	-20℃*15min～80℃*15min 100cycles	MIL-STD-202G	0/64
2	High Temperature Storage	Ta= 100℃ 1000hrs	JEITA ED-4701 200 201	0/64
3	Low Temperature Storage	Ta= -40℃ 1000hrs	JEITA ED-4701 200 202	0/64
4	High Temperature High Humidity Storage	Ta=60℃ RH=90% 1000hrs	JEITA ED-4701 100 103	0/64
5	Temperature Cycle	-20℃~25℃~80℃~25℃ 30min~5min~30min~5min 100 cycles	JEITA ED-4701 100 105	0/64
6	Resistance to Soldering Heat	Tsld = 260℃, 10sec. 2times	JEITA ED-4701 300 301	0/64
7	Room temp Life Test	Ta＜35℃, IF:Typical current, 3000hrs	JESD22-A 108D	0/22

Criteria for Judging the Damage:

Item	Symbol	Test Condition	Limit	
			Min	Max
Luminous Intensity	IV	DC=5V, Typical current	Init. Value*0.7	---
Resistance to Soldering Heat	---	DC=5V, Typical current	No dead lights or obvious damage	